Notice of Allowability	Application No.	Applicant(s)	
	10/623,146	ADAMO ET AL.	
	Examiner	Art Unit	
	Tuan V. Thai	2186	
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in thi or other appropriate communic IGHTS. This application is subjection	s application. If not included ation will be mailed in due course. THIS	
1. \boxtimes This communication is responsive to <u>amendment filed 11/</u>	<u>10/2006</u> .		
2. $igtimes$ The allowed claim(s) is/are <u>1-20 renumbered as 1-1416-20</u>	0 and 15 respectively.		
 Acknowledgment is made of a claim for foreign priority unally a)).	
□ Certified copies of the priority documents have □ Certified copies of the priority documents have		_	
3. ☐ Copies of the certified copies of the priority do			
International Bureau (PCT Rule 17.2(a)).	cuments have been received in	this national stage application from the	
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a re MENT of this application.	eply complying with the requirements	
 A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give 	nitted. Note the attached EXAMII es reason(s) why the oath or de	NER'S AMENDMENT or NOTICE OF claration is deficient.	
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.		
(a) ☐ including changes required by the Notice of Draftspers		PTO-948) attached	
1) hereto or 2) to Paper No./Mail Date		,	
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in t	he Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the d he header according to 37 CFR 1.	rawings in the front (not the back) of 121(d).	
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 	SIT OF BIOLOGICAL MATERIA FOR THE DEPOSIT OF BIOLO	AL must be submitted. Note the GICAL MATERIAL.	
Attachment(s) 1. □ Notice of References Cited (PTO-892)		nal Patent Application	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Sumn		
 Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 		Paper No./Mail Date 7. ☐ Examiner's Amendment/Comment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's Stat	ement of Reasons for Allowance	
		TUAN V. THAI PRIMARY EVANDUES	
		(Rail) 2100	
U.S. Patent and Trademark Office PTOL-37 (Rev. 08-06)	otice of Allowability	Part of Paper No./Mail Date 2006121	

Part of Paper No./Mail Date 20061219

-Page 2-

Application/Control Number: 10/623,146

Art Unit: 2186

IN THE UNITED STATES PATENT AND

In re application of: Yamamoto et al. Group: 2186

Serial No.: 10/623,146 Examiner: Tuan Thai

For: PROCESS FOR MANAGING SYSTEM STACKS IN MICROCONTROLLERS,

CORRESPONDING DEVICE AND COMPUTER PROGRAM PRODUCT.

- This action is responsive to amendment filed November 10,
 Claims 1-20 are now allowed.
- 2. The rejection of claim 1 under 35 USC § 101 is withdrawn due to the amendment filed November 10, 2006.

REASONS FOR ALLOWANCE

3. The following is an Examiner's Statement of Reasons for Allowance:

The prior arts of record do not teach nor suggest, either alone or in combination, all the limitations of the amended claims of the current invention (claims 1, 6, 14 and 15) particularly a method and system having a stack management device associated with a microcontroller having a number of interrupts wherein the device having a first memory stack comprising a register for a Program Counter signal, a second memory stack for a Condition Code Register signal that includes a number of bits,

Application/Control Number: 10/623,146

Art Unit: 2186

the second memory stack being made up of a bank of memory elements equal in number to the bits of the Condition Code Register signal times the number of interrupts of the microcontroller, and at least one manager module configured to cause the first and second memory stacks to function in parallel by respective stack pointer signals (claim 6). Similarly for claims 1, 14 and 15. Claims 2-5, 7-13, 16-19 and 20 are also allowable since they are depended upon the indicated allowable claims 1, 6, 15 and 14 respectively.

Any comments considered necessary by Applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan V. Thai whose telephone number is (571)-272-4187. The examiner can normally be reached on from 6:30 A.M. to 4:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mathew M. Kim can be reached on (571)-272-4182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application

Application/Control Number: 10/623,146

Art Unit: 2186

may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TVT/November 22, 2006

PRIMARY EXAMINER

Group 2100